

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
Application Branch

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J1040 U.S. PTO
10/061302



In re Patent Application of
Applicant : Akira YOSHIDA ET AL.

Serial No. : Not Yet Assigned

Filed : February 4, 2002

For : SIGNAL COMPENSATION CIRCUIT AND)
DEMODULATING CIRCUIT WITH HIGH-)
SPEED AND LOW-SPEED FEEDBACK)
LOOPS)

Attorney Docket: 31869-178004)

INFORMATION
DISCLOSURE
STATEMENT

February 4, 2002

Assistant Commissioner for Patents
Washington, D.C. 20231



26694

PATENT TRADEMARK OFFICE

Sir:

This is an Information Disclosure Statement submitted under 37 C.F.R. 1.97 ff and 1.98 ff within the time specified in 37 C.F.R. 1.97(b).

Attached are copies of two U.S. Patents considered relevant to the subject application and discussed on page 1 of the specification. The publications are listed on an enclosed PTO Form 1449.

As all requirements of 37 C.F.R. 1.97 ff and all official guidelines pertaining to Information Disclosure Statements have been complied with, it is respectfully requested that the Examiner consider the publication and make it of record.

Respectfully submitted,

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FORM PTO-1449 INFORMATION DISCLOSURE CITATION		Atty Docket: 31869-178004		Application No.:	
		Applicant: Akira YOSHIDA ET AL.			
		Filing Date: February 4, 2002		Group:	

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U.S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Sub - Class	Filing Date
	AA	6,104,238	Aug 15, 2000	Mattisson et al.			
	AB	5,412,692	May 2, 1995	Uchida			
	AC						
FOREIGN PATENT DOCUMENTS							
Examiner Initial					Class	Sub - Class	Translation Yes <input type="checkbox"/> No <input type="checkbox"/>
	AF						
	AG						
	AH						
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
	AO						
	AP						
	AQ						

Examiner	Date Considered
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.	